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**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANT(S): Seung-hoon Lee et al.

DOCKET: 8021-182 (SS-18122-US)

SERIAL NO.: 10/731,841

GROUP ART UNIT: 2824

FILED: December 9, 2003

EXAMINER: Luu, Pho M.

**FOR: SENSE AMPLIFYING CIRCUIT FOR A SEMICONDUCTOR  
MEMORY WITH IMPROVED DATA READ ABILITY AT A  
LOW SUPPLY VOLTAGE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO RESTRICTION REQUIREMENT**

Sir:

In response to the Office Action dated July 12, 2005, Applicants provisionally  
elect the claims of Group I (i.e., claims 1-8 and 18-27) with traverse.

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**CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)**

I hereby certify that this correspondence is being deposited with the United States Postal Service  
as first class mail, postpaid in an envelope, addressed to the: Assistant Commissioner for Patents,  
Washington, D.C. 20231 on August 11, 2005.

Dated: \_\_\_\_\_

8/11/05

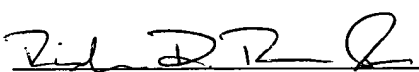
Richard D. Ratchford Jr.

While the devices of Groups I and II may be distinct as set forth by the Examiner, Applicants believe that simultaneous examination will not present an undue burden. For example, the claims of Group I are drawn to a circuit for use in a semiconductor memory and the claims of Group II are drawn to a sense amplifying circuit for use in a semiconductor memory. Both groups of claims are believed to be found commonly in class 365.

As such, Applicants respectfully disagree that the claims of Group II are drawn solely to a sense amplifying circuit in class 327 as indicated by the Examiner. Indeed, even if the claims of Group II are found in class 327, the class is referenced as being related to class 365. Under such circumstances, the Examiner is encouraged to maintain the claims of Groups I and II in the same application. See, MPEP § 803.

Early and favorable consideration of the application is earnestly solicited.

Respectfully submitted,

By:   
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